

## OFFICIAL UNDERGRADUATE COURSE OUTLINE FORM

**Note:** The University reserves the right to amend course outlines as needed without notice.

<b>Course Code and Number:</b> ELTR 150		<b>Number of Credits:</b> 3 <a href="#">Course credit policy (105)</a>													
<b>Course Full Title:</b> Solid State Electronic Devices <b>Course Short Title:</b>															
<b>Faculty:</b> Faculty of Applied and Technical Studies		<b>Department (or program if no department):</b> Electronics													
<b>Calendar Description:</b> Introduction to semiconductor devices and their applications. This course explains how electronic circuits work and how to analyze, design, modify, and combine them to perform complex functions. Students will analyze and design common operational amplifier (op-amp) circuits and examine the op-amps non-ideal characteristics in terms of circuit performance.															
<b>Prerequisites (or NONE):</b>		None.													
<b>Corequisites (if applicable, or NONE):</b>		None.													
<b>Pre/corequisites (if applicable, or NONE):</b>		ELTR 100 and ELTR 105.													
<b>Antirequisite Courses</b> <i>(Cannot be taken for additional credit.)</i> Former course code/number: Cross-listed with: Equivalent course(s): <i>(If offered in the previous five years, antirequisite course(s) will be included in the calendar description as a note that students with credit for the antirequisite course(s) cannot take this course for further credit.)</i>		<b>Course Details</b> Special Topics course: <b>No</b> <i>(If yes, the course will be offered under different letter designations representing different topics.)</i> Directed Study course: <b>No</b> <i>(See <a href="#">policy 207</a> for more information.)</i> Grading System: <b>Letter grades</b> Delivery Mode: <b>May be offered in multiple delivery modes</b> Expected frequency: <b>Winter only</b> Maximum enrolment (for information only): <b>36</b>													
<b>Typical Structure of Instructional Hours</b> <table border="1"> <tr> <td>Lecture/seminar</td> <td>30</td> </tr> <tr> <td>Supervised laboratory hours (design lab)</td> <td>15</td> </tr> <tr> <td></td> <td></td> </tr> <tr> <td></td> <td></td> </tr> <tr> <td></td> <td></td> </tr> <tr> <td><b>Total hours</b></td> <td><b>45</b></td> </tr> </table>		Lecture/seminar	30	Supervised laboratory hours (design lab)	15							<b>Total hours</b>	<b>45</b>	<b>Prior Learning Assessment and Recognition (PLAR)</b> PLAR is available for this course.	
Lecture/seminar	30														
Supervised laboratory hours (design lab)	15														
<b>Total hours</b>	<b>45</b>														
<b>Scheduled Laboratory Hours</b> Labs to be scheduled independent of lecture hours: <input type="checkbox"/> No <input checked="" type="checkbox"/> Yes		<b>Transfer Credit</b> <i>(See <a href="#">bctransferguide.ca</a>.)</i> Transfer credit already exists: <b>No</b> Submit outline for (re)articulation: <b>No</b> <i>(If yes, fill in <a href="#">transfer credit form</a>.)</i>													
<b>Department approval</b>		<b>Date of meeting:</b> November 9, 2021													
<b>Faculty Council approval</b>		<b>Date of meeting:</b> November 18, 2021													
<b>Undergraduate Education Committee (UEC) approval</b>		<b>Date of meeting:</b> January 28, 2022													

**Learning Outcomes** *(These should contribute to students' ability to meet program outcomes and thus Institutional Learning Outcomes.)*

Upon successful completion of this course, students will be able to:

1. Describe and analyze the electrical characteristics of diodes, BJT, JFET and MOSFET.
2. Analyze the different Transistor biasing techniques and calculate bias voltages and currents.
3. Analyze BJT/ MOSFET both as amplifier and switch.
4. Describe and measure important specifications of op-amps.
5. Analyze the frequency response of amplifiers using common test equipment.
6. Analyze positive and negative feedback with operational amplifiers and other circuits.
7. Design and test regulated power supply circuit.
8. Explain the design and application of active filters.

**Recommended Evaluation Methods and Weighting** *(Evaluation should align to learning outcomes.)*

Final exam:	40%	Quizzes/tests:	20%	%
Assignments:	10%	Lab work:	30%	%

**Details:**

**NOTE:** The following sections may vary by instructor. Please see course syllabus available from the instructor.

**Texts and Resource Materials** *(Include online resources and Indigenous knowledge sources. [Open Educational Resources](#) (OER) should be included whenever possible. If more space is required, use the [Supplemental Texts and Resource Materials form](#).)*

Type	Author or description	Title and publication/access details	Year
1. Textbook	Thomas L. Floyd	Electronic Devices (Electron flow version)	2017
2.			
3.			
4.			
5.			

**Required Additional Supplies and Materials** *(Software, hardware, tools, specialized clothing, etc.)***Course Content and Topics**

- Semiconductors
- Diodes, diode applications, special action diodes, diode biasing
- Bipolar Junction Transistors (BJTs), transistor biasing
- BJT amplifiers, BJT power amplifiers, multistage amplifiers, differential amplifier
- Field Effect Transistors (FETs), FET amplifiers, amplifier frequency response
- Thyristors, silicon-controlled rectifiers, diacs, triacs, unijunction transistors
- Operational amplifiers, op-amp circuits
- Active filters, oscillators, voltage regulators