

ORIGINAL COURSE IMPLEMENTATION DATE: REVISED COURSE IMPLEMENTATION DATE: September 2020

COURSE TO BE REVIEWED (six years after UEC approval): January 2026 Course outline form version: 05/18/2018

OFFICIAL UNDERGRADUATE COURSE OUTLINE FORM

Note: The University reserves the right to amend course outlines as needed without notice.

Course Code and Number: ELTR 190		Number of Credits: 3 Course credit policy (105)					
Course Full Title: Electronics Capstone Project							
Course Short Title: Electronics Capstone Project							
(Transcripts only display 30 characters. Departments may recommend a short title if one is needed. If left blank, one will be assigned.)							
Faculty: Faculty of Applied and Technical Studies Department (example)				or program if no department): Electronics			
Calendar Description:							
Apply knowledge gained in prior courses to specific project. Design, build, and assemble the prototype and generate manufacturing documents. Communicate the methods and processes of the project in a presentation.							
Prerequisites (or NONE): ELTR 100, ELTR 110, ELTR 13			R 130, ai	₹ 130, and ELTR 140.			
Corequisites (if applicable, or NONE):	quisites (if applicable, or NONE): ELTR 150, ELTR 160, ELTR 1			₹ 170, and ELTR 180.			
Pre/corequisites (if applicable, or NONE): None.							
Antirequisite Courses (Cannot be taken for	additional cre	ədit.)	Special Topics (Double-click on boxes to select.)				
Former course code/number:			This course is offered with different topics:				
Cross-listed with:			\square No \square Yes (If yes, topic will be recorded when offered.)				
Dual-listed with:			Independent Study				
Equivalent course(s):			If offered as an Independent Study course, this course may				
(If offered in the previous five years, antirequisite course(s) will be included in the calendar description as a note that students with credit for the antirequisite course(s) cannot take this course for further credit.)			be repeated for further credit: (If yes, topic will be recorded.) ⊠ No □ Yes, repeat(s) □ Yes, no limit				
				Transfer Credit			
Typical Structure of Instructional Hours			Transfer credit already exists: (See <u>bctransferguide.ca</u> .)				
Lecture/seminar hours	0		No ☐ Yes Submit outline for (re)articulation:				
Tutorials/workshops							
Supervised laboratory hours		45	🖾 No	Image: System Grading System			
Experiential (field experience, practicum, internship, etc.)			Grading				
Supervised online activities			∐ Letter Grades □ Credit/No Credit				
Other contact hours:			Maximu	um enrolment (for infor	mation only): 36		
	Total hours	45	Expect	Expected Frequency of Course Offerings:			
Labs to be scheduled independent of lecture hours: No Yes			Winter only (Every semester, Fall only, annually, etc.)				
Department / Program Head or Director:				Date approved:	November 2019		
Faculty Council approval				Date approved:	November 14, 2019		
Dean/Associate VP: John English				Date approved:	November 14, 2019		
Campus-Wide Consultation (CWC)				Date of posting:	January 17, 2020		
Undergraduate Education Committee (UEC	C) approval			Date of meeting:	January 31, 2020		

Learning Outcomes

Upon successful completion of this course, students will be able to:

- Apply all acquired skills to design, assemble a PCB based electronics project.
- Design a circuit schematic for a specific function.
- Assemble a prototype circuit using a solderless breadboard.
- Design a PCB layout from a circuit schematic.
- Assemble a PCB circuit.
- Present and explain technical information to an audience.
- Employ iterative and modular design and development techniques.
- Work in team and apply project management tools effectively.
- Generate all the documentation required for manufacturing of the product.

Prior Learning Assessment and Recognition (PLAR)

Yes INO, PLAR cannot be awarded for this course because

Typical Instructional Methods (Guest lecturers, presentations, online instruction, field trips, etc.; may vary at department's discretion.) Lab Work

NOTE: The following sections may vary by instructor. Please see course syllabus available from the instructor.

Typical Text(s) and Resource Materials (If more space is required, download Supplemental Texts and Resource Materials form.)

	Author (surname, initials)	Title (article, book, journal, etc.)	Current ed. Publisher	Year
1.	No Textbook Required			
2.				
3.				
4.				
5.				
Re	quired Additional Supplies a	nd Materials (Software, hardware, tools, spec	ialized clothing, etc.)	

Typical Evaluation Methods and Weighting

Final exam:	%	Assignments:	20%	Field experience:		Portfolio:	%
Midterm exam:	%	Project:		Practicum:	%	Other:	%
Quizzes/tests:	30%	Lab work:	50%	Shop work:	%	Total:	100%

Details (if necessary):

Typical Course Content and Topics

Project should have minimum DC power source component build and additional circuitry for display and control.