

ORIGINAL COURSE IMPLEMENTATION DATE: REVISED COURSE IMPLEMENTATION DATE: September 2020

COURSE TO BE REVIEWED (six years after UEC approval): Course outline form version: 05/18/2018 January 2026

OFFICIAL UNDERGRADUATE COURSE OUTLINE FORM

Note: The University reserves the right to amend course outlines as needed without notice.

Course Code and Number: ELTR 261		Number of Credits: 3 Course credit policy (105)				
Course Full Title: Programmable Logic Cor Course Short Title: PLC II	ntrollers II					
(Transcripts only display 30 characters. Departments may recommend a short title if one is needed. If left blank, one will be assigned.)						
Faculty: Faculty of Applied and Technical St	Department (or program if no department): Electronics					
Calendar Description:						
Learn to assemble, test, debug, and operate learn to read and interpret ladder logic diagra the safe work practices and design machine of	PLC integrat ims and sequ control progr	tions with electro uential function o ams and integra	o-pneuma charts. Pr ite systen	tic and electro-mechanic oject-based learning whe ns.	al systems. Students will re students get exposure to	
Prerequisites (or NONE):	ELTR 211.					
Corequisites (if applicable, or NONE):	NONE					
Pre/corequisites (if applicable, or NONE):	NONE					
Antirequisite Courses (Cannot be taken for additional credit.)			Special Topics (Double-click on boxes to select.)			
Former course code/number:			This course is offered with different topics:			
Cross-listed with:			\square No \square Yes (If yes, topic will be recorded when offered.)			
Dual-listed with:			Indepe	ependent Study		
Equivalent course(s):			If offere	fered as an Independent Study course, this course may		
(If offered in the previous five years, antirequisite course(s) will be included in the calendar description as a note that students with credit for the antirequisite course(s) cannot take this course for further credit.)			be repeated for further credit: (If yes, topic will be recorded.) ⊠ No □ Yes, repeat(s) □ Yes, no limit			
			Transfer Credit			
Typical Structure of Instructional Hours			Transfer credit already exists: (See <u>bctransferguide.ca</u> .)			
Lecture/seminar hours	30	🖾 No	🛛 No 🔲 Yes			
Tutorials/workshops		Submit outline for (re)articulation:				
Supervised laboratory hours	15	☑ No □ Yes (If yes, fill in transfer credit form.) Grading System				
Experiential (field experience, practicum, internship, etc.))
Supervised online activities			🛛 Lette	er Grades 🛛 Credit/No	Credit	
Other contact hours:			Maxim	um enrolment (for infor	mation only): 20	
	Total hour	s 45	Expect	ed Frequency of Cours	Offerings:	
Labs to be scheduled independent of lecture	hours: 🗌 N	lo 🛛 Yes	Winter	only (Every semester, Fa	ll only, annually, etc.)	
Department / Program Head or Director:				Date approved:	November 2019	
Faculty Council approval				Date approved:	November 14, 2019	
Dean/Associate VP: John English			Date approved:	November 14, 2019		
Campus-Wide Consultation (CWC)				Date of posting:	January 17, 2020	
Undergraduate Education Committee (UEC) approval			Date of meeting:	January 31, 2020		

Learning Outcomes

Upon successful completion of this course, students will be able to:

- Assemble, test, debug, and operate programmable logic controller integrations with electropneumatic and electro-mechanical systems.
- Design, develop, test, and implement basic programmable logic controller systems to solve applied technical problem.
- Read and interpret ladder logic diagrams, sequential function charts, and related programs.
- Convert programs and diagrams between ladder logic, function block, and sequential flow charts.
- Employ programmable logic controller programs to control machines and processes typically used in agricultural and industrial applications.
- Employ safety-oriented methods and processes to the design of machine control programs and integrations.

Prior Learning Assessment and Recognition (PLAR)

Yes No, PLAR cannot be awarded for this course because

Typical Instructional Methods (*Guest lecturers, presentations, online instruction, field trips, etc.; may vary at department's discretion.*) Lecture and Lab Work

NOTE: The following sections may vary by instructor. Please see course syllabus available from the instructor.

Typical Text(s) and Resource Materials (If more space is required, download Supplemental Texts and Resource Materials form.)

	Author (surname, initials)	Title (article, book, journal, etc.)	Current ed.	Publisher	Year	
1.	No textbook required	Worksheets and lecture notes provided by Instructor	\bowtie			
2.						
3.						
4.						
5.						
Re	Required Additional Supplies and Materials (Software, hardware, tools, specialized clothing, etc.)					

Typical Evaluation Methods and Weighting

Final exam:	%	Assignments:	%	Field experience:		Portfolio:	%
Midterm exam:	%	Project:	40%	Practicum:	%	Other:	%
Quizzes/tests:	30%	Lab work:	30%	Shop work:	%	Total:	100%

Details (if necessary):

Typical Course Content and Topics

- Intermediate function block diagram programming
- Saftey-oriented design and implementation
- Reading analog inputs, controlling analog outputs
- Sequential function chart programming
- Ladder logic diagram programming
- Program conversion (function block/ladder/sequential)